

Dkt. No. 24705/99
(NEC.153)



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of Allowed: March 9, 2004

Ritsuko IWASAKI Confirmation No. : 6172

Serial No.: 09/496,421 Group Art Unit: 2815

Filing Date: February 2, 2000 Examiner: Lee, Eugene

For: SEMICONDUCTOR DEVICE HAVING AN IMPROVED LAYOUT PATTERN OF
PAIR TRANSISTORS

Honorable Commissioner of Patents
Alexandria, Virginia 22313-1450

REQUEST FOR ISSUE DATE

Sir:

The undersigned respectfully requests an Issue Notification for the above-identified allowed application. The issue fee was paid on May 28, 2004. Subsequently, on August 17, 2004, the initialed PTO Form 1449 for the Information Disclosure Statement filed May 20, 2004 was received. Since that date, we have received no further communication from the Patent Office in this matter.

Applicant respectfully requests that an Issue Notification be provided indicating the issue date for the Letters Patent in the above-identified allowed application.

If there are any questions on this matter, please direct all telephone calls to the number shown below.

The Commissioner is authorized to charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

Sean M. McGinn, Esq.
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Date: 11/15/04

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